

REMARKS

Claims 13-14, 16-17, and 25-34 are currently pending. Claims 13, 14, 16, 17, and 25-33 have been amended. New claims 35-40 have been added.

In view of the present amendments and the arguments presented in the previous response, Applicant submits that claims 13-14, 16-17 and 25-34 are now in condition for allowance as each claim recites the use of flash memory and the displaying of the operational status of this flash memory.

To review the arguments previously submitted, Applicant summarizes as below:

1. Yamagami et al. is directed towards flash memory but does not describe a display means.
2. Yoshito is directed towards traditional bit memory which is an art completely different from the flash memory of Yamagami and, therefore, should not be combined with Yamagami.
3. Yoshito pertains to display of raw bit storage capacity while the present invention displays an operational status of the flash memory and does not provide a display of remaining available storage capacity.

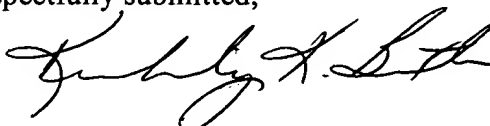
Conclusion: Yamagami and Yoshito are not analogous arts and should not be combined to reach a conclusion of obviousness and, further, Yoshito does not disclose the display of an operational status of a flash memory.

Newly added claims 35–40 also include the elements of a flash memory and the display of an operational status of the flash memory. As such, Applicant submits that these claims are also in condition for allowance.

In view of the foregoing, it is submitted that this application is in condition for allowance. Favorable consideration and prompt allowance of the application are respectfully requested.

The Examiner is invited to telephone the undersigned if the Examiner believes it would be useful to advance prosecution.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'Kimberly K. Baxter', written in a cursive style.

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